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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/539,637	03/30/2000	Fong-Shek Lam	10559/170001/P8263	8485

20985 7590 04/12/2002

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EXAMINER

WALLACE, SCOTT A

ART UNIT

PAPER NUMBER

2672

DATE MAILED: 04/12/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	09/539,637	LAM ET AL.
	Examiner	Art Unit
	Scott Wallace	2672

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on _____.
- 2a) This action is **FINAL**. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-23 is/are pending in the application.
 - 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1-23 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) The proposed drawing correction filed on _____ is: a) approved b) disapproved by the Examiner.

If approved, corrected drawings are required in reply to this Office action.
- 12) The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.
- 14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
 - a) The translation of the foreign language provisional application has been received.
- 15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s). _____.
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____.	6) <input type="checkbox"/> Other: _____

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-20 and 22-23 are rejected under 35 U.S.C. 102(b) as being anticipated by Gaglianello et al., U.S. Patent No. 5,526,024.
3. As per claim 1, Gaglianello teaches a method comprising: setting an indicator in a buffer (column 1 lines 50 –67 and column 2 lines 1-11); reading pixel data for a current video line from the buffer (column 2 lines 1-11); determining when the pixel data reaches the indicator (column 2 lines 1-11); and loading data for the next video line into the buffer (column 2 lines 1-11).
4. As per claim 2, Gaglianello teaches further comprising setting the indicator at approximately a middle of the buffer (column 1 lines 61-65).
5. As per claim 3, Gaglianello teaches further comprising loading data for the next video line to replace data for the current video line in the buffer (column 2 lines 1-11).
6. As per claim 4, Gaglianello teaches further comprising processing the current video line data for display (column 2 lines 1-11).
7. As per claim 5, Gaglianello teaches further comprising displaying the processed video line data (column 2 lines 1-11).
8. As per claim 6, Gaglianello teaches further comprising creating a video overlay from the processed video line data (column 6 lines 10-23).
9. As per claim 7, Gaglianello teaches further comprising positioning the pixel data on an active display to create a video overlay (column 6 lines 10-23).
10. As per claim 8, Gaglianello teaches a method of processing video overlay data comprising: reading video data for a current video line from a buffer (column 2 lines 1-11); detecting the position in the

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buffer the video data is located (column 1 lines 60-67 and column 2 lines 1-11); loading data for the next video line into the buffer when the video data for the current video line is located at a predetermined position (column 2 lines 1-11).

11. As per claim 9, Gaglianello teaches further comprising setting the predetermined position at a position before all the current video line data is read (column 1 lines 60-67 and column 2 lines 1-11).
12. As per claim 10, Gaglianello teaches further comprising setting the predetermined position at approximately a midpoint of the buffer (column 1 lines 60-65).
13. As per claim 11, Gaglianello teaches further comprising loading data for the next video line to replace data for the current video line in the buffer (column 2 lines 1-11).
14. As per claim 12, Gaglianello teaches further comprising processing the current video line data for display (column 2 lines 1-11).
15. As per claim 13, Gaglianello teaches further comprising displaying the processed video line data (column 2 lines 1-11).
16. As per claim 14, Gaglianello teaches a overlay display processor comprising: a buffer having a plurality of memory locations, the buffer adapted to provide data to a display (column 1 lines 50-67); and an indicator positioned at a predetermined memory location in the buffer, wherein the buffer begins to read data for a next video data line when the buffer provides data from the indicator memory location (column 2 lines 1-11).
17. As per claim 15, Gaglianello teaches further comprising graphic memory which provides the video pixel data to the buffer (column 1 lines 50-67).
18. As per claim 16, Gaglianello teaches wherein the buffer provides data to the display for a current video line (column 2 lines 1-11).
19. As per claim 17, Gaglianello teaches wherein the indicator is located at a position at approximately a midpoint of the buffer (column 1 lines 60-65).
20. As per claim 18, Gaglianello teaches a overlay display system comprising: video memory which stores video data (column 1 lines 50-60); an overlay processing engine comprising: a buffer which receives the video data from the memory (column 1 lines 50-67); video processing circuitry for preparing

the video data in the buffer to be displayed (column 2 lines 1-11); and a display which receives the processed data from the overlay processing engine, wherein the buffer begins to read data for a next video data line when the buffer provides a predetermined amount of data to the display for a current video data line (column 2 lines 1-11 and column 6 lines 10-23).

21. As per claim 19, Gaglianello teaches wherein the predetermined amount of data is approximately half the data comprising the current video data line (column 1 lines 60-67).
22. As per claim 20, Gaglianello teaches wherein the overlay processing engine provides data to the display to create a video overlay (column 6 lines 10-23).
23. As per claim 22, Gaglianello teaches a program storage device readable by a machine comprising instructions that cause the machine to: set an indicator in a buffer (column 1 lines 50-67); read pixel data for a current video line from the buffer (column 1 lines 50-67); determine when the pixel data reaches the indicator (column 2 lines 1-11); and load data for the next video line into the buffer (column 2 lines 1-11).
24. As per claim 23, Gaglianello teaches wherein the instructions further cause the machine to set the indicator at approximately a middle of the buffer (column 1 lines 60-65).

Claim Rejections - 35 USC § 103

25. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

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26. Claim 21 is rejected under 35 U.S.C. 103(a) as being unpatentable over Gaglianello et al. in view of Kagawa et al., U.S. Patent No. 5,917,959.

27. As per claim 21, Gaglianello teaches all the limitations of claim 18 as seen above. Gaglianello does not teach wherein the video processing circuitry includes pixel color conversion and adjustment. Kagawa teaches this in column 4 lines 61-65). It would have been obvious to one of ordinary skill in the art to use the pixel color conversion device of Kagawa with the system of Gaglianello. This would have been obvious because it would have allowed Gaglianello to show video in color.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to **Scott Wallace** whose telephone number is **703-605-5163**.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **Michael Razavi**, can be reached at 703-305-4713.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231

or faxed to:

(703) 872-9314 (for Technology Center 2600 only)

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (703) 306-0377.

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MICHAEL RAZAVI
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600